

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Juergen Einspinner et al.
Serial No.: Unknown
(Priority Application No. DE 102 45 452.3)
(International Application No. PCT/DE03/03208)
Filed: Herewith
(Priority Date 27 September 2002)
(International Filing Date 26 September 2003)
Docket No.: I431.125.101/FIN 249 PCT/US
Title: **METHOD FOR DETERMINING THE ARRANGEMENT OF CONTACT
AREAS ON THE ACTIVE TOP SIDE OF A SEMICONDUCTOR CHIP**

INFORMATION DISCLOSURE STATEMENT

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached form 1449 be considered by the Examiner and made of record. Any required copies of patents, publications or other documents are enclosed for the Examiner's review. Pursuant to the provisions of M.P.E.P. 609, Applicants further request a copy of the 1449 form, marked as being considered and initialed by the Examiner, be returned with the next Official Communication.

Since this Information Disclosure Statement is being submitted within three months of filing national application; or date of entry of national application; or before the mailing date of the first Office Action on the merits, a fee has not been enclosed. However, if such fee is required, the Patent Office is hereby authorized to charge Deposit Account No. 500471 for fees as set forth under 37 C.F.R. 1.17(p).

Information Disclosure Statement

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Applicants respectfully request consideration of these references during prosecution of the above-identified matter. The Examiner is invited to contact the Applicants' representative at the below-listed telephone number if there are any questions regarding this Communication or the tendered references.

Respectfully submitted,

Juergen Einspenner et al.,

By their attorneys,

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CERTIFICATE UNDER 37 C.F.R. 1.10:

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Date of Deposit: March 16, 2005

The undersigned hereby certifies that this paper or papers, as described herein, are being deposited in the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to: Mail Stop PCT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By Vanessa Carels
Name: Vanessa Carels

FORM PTO-1449	Docket No.: I431.125.101/FIN 249 PCT/US	Serial No.: Unknown Priority Appln. No. DE 102 45 452.3 Int'l Appln. No. PCT/DE03/03208
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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
	5,331,572	07/1994	Takahashi			
	5,498,767	03/1996	Huddleston et al.			
	5,608,638	03/1997	Tain et al.			
	5,627,408	05/1997	Kusumi			
	5,828,116	10/1998	Ao			
	6,133,623	10/2000	Otsuki et al.			
	6,357,036	03/2002	Eka et al.			
	6,405,357	06/2002	Chao et al.			

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Class	Sub Class	Translated Yes No
2004-085947	03/2004	Japan			Yes (Abstract Only)
59-139660	08/1984	Japan			Yes (Abstract Only)
					Yes

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Ho Chew Choy Paul, SG, "Pad Placement Algorithm for Four-sided Packages", Siemens Technik Report, JG 4 Nr. 10, January 2001, pgs. 142-150.

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.